



FEATURES

- Passive resistance output
- Ceramic package

DESCRIPTION

The NSL-5532 is a CdS photoconductive cell on a TO-5 ceramic substrate. The photocell is encapsulated with epoxy for moisture resistance.

APPLICATIONS

- Industrial

ABSOLUTE MAXIMUM RATING

(TA)= 23°C UNLESS OTHERWISE NOTED

SYMBOL	PARAMETER	MIN	MAX	UNITS
V _P	Voltage (peak AC or DC)		320	V
P _d	Power Dissipation @ 25°C (1)		125	mW
T _{Op}	Operating Temperature	-60	+75	°C
T _{Stg}	Storage Temperature	-60	+75	°C
T _S	Soldering Temperature (2)		+260	°C

Note:

- (1) Derate linearly to 0 at 75°C
- (2) >0.08" from case for <5 sec.
- (3) Cells light adapted at 30 to 50 Ftc for 16 hrs minimum prior to electrical tests.

RELIABILITY

Contact API for recommendations on specific test conditions and procedures.

ELECTRO-OPTICAL CHARACTERISTICS

(TA)= 23°C, UNLESS OTHERWISE NOTED

SYMBOL	CHARACTERISTIC	TEST CONDITIONS	MIN	TYP	MAX	UNITS
R _L	Light Resistance	2 ftc., 2854°K (3)	110	165	220	KΩ
		100 ftc., 2854°K		10		KΩ
R _D	Dark Resistance	5 sec after removal of test light.	11			MΩ
λ _P	Spectral Peak			550		nm